

Circuit Theory

Richard Pates

Who cares?

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—Malcolm Smith

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...turned into the major scientific interest of Kalman's last decade...

—Malcolm Smith

Who cares?



Who cares?

- A very useful class of models:
 - Can describe many phenomena
 - Many analogues with other physical domains
 - Many useful control architectures (PID, phase lead/lag)
- Inspires useful theory:
 - Lyapunov functions
 - Energy dissipation and passivity
 - ...

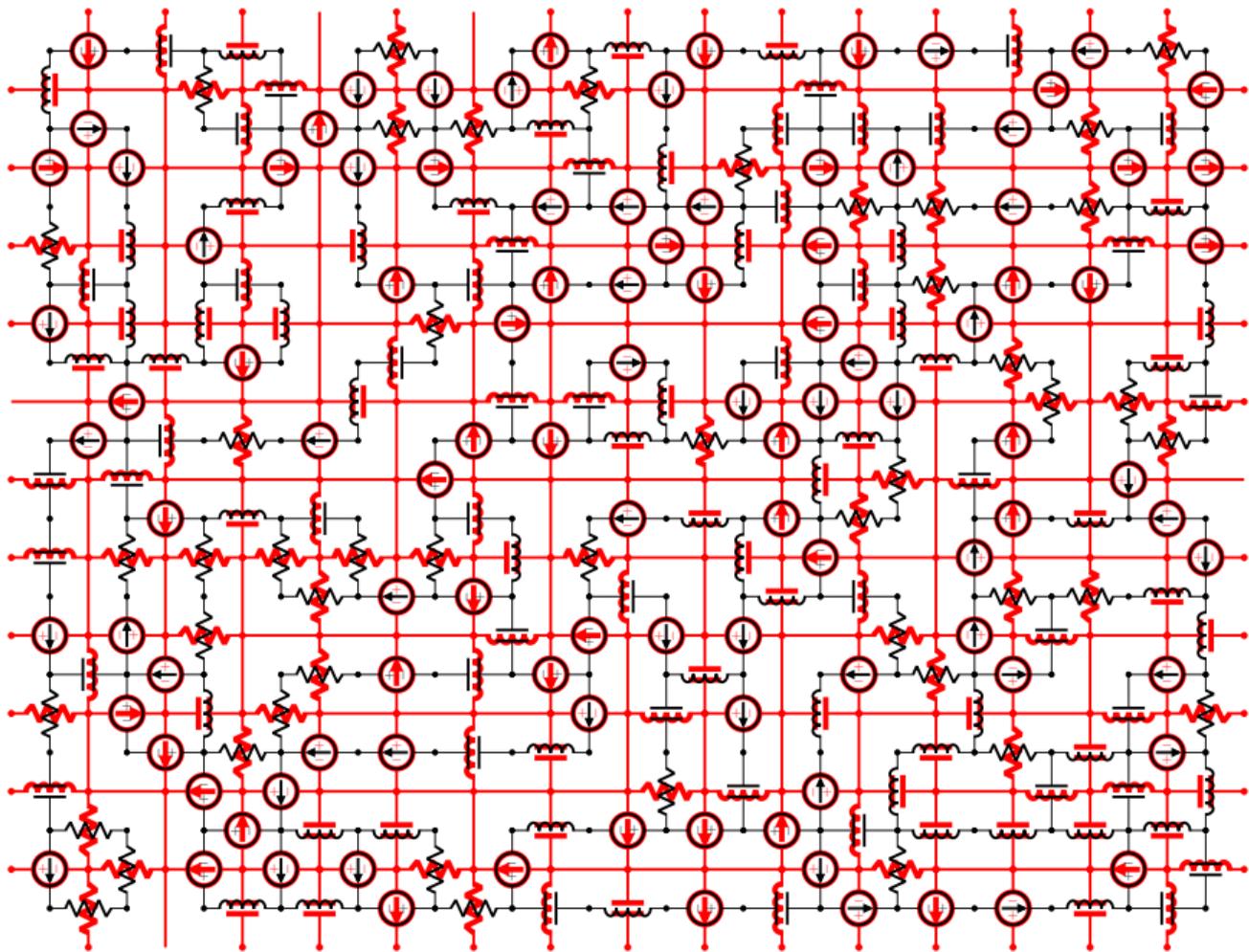
Today's Lecture

- Modelling electrical networks
- Analogues
- Network synthesis and state-space models
- Unsolved problems

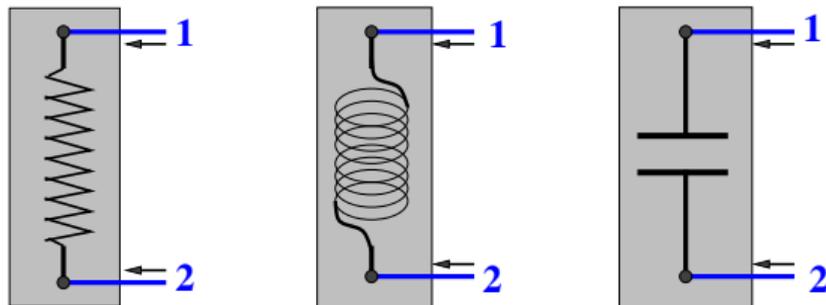
Modelling circuits

A picture, with a clear mathematical meaning:

- edges \iff differential equations, driving points
- topology \iff algebraic equations

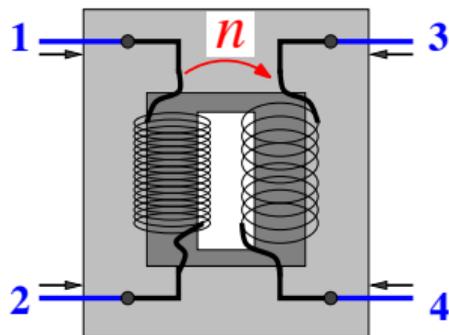


The passive elements



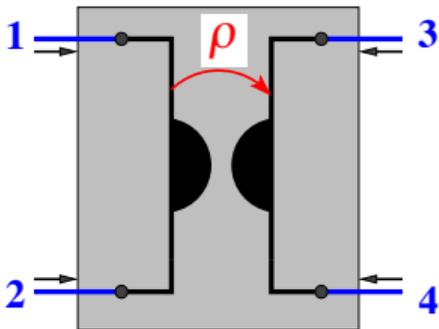
$$\left[P_k \left(\frac{d}{dt} \right) \mid Q_k \left(\frac{d}{dt} \right) \right] \begin{bmatrix} i_{\text{int},k} \\ v_{\text{int},k} \end{bmatrix} = 0$$

The passive elements



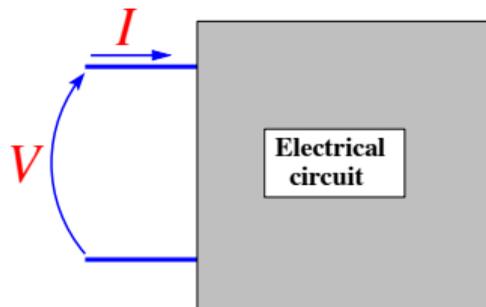
$$\left[\begin{array}{cc|cc} 0 & 0 & n & -1 \\ 1 & n & 0 & 0 \end{array} \right] \begin{bmatrix} i_{\text{int},j} \\ i_{\text{int},k} \\ \hline v_{\text{int},j} \\ v_{\text{int},k} \end{bmatrix} = 0$$

The passive elements



$$\left[\begin{array}{cc|cc} 0 & -\rho & 1 & 0 \\ \rho & 0 & 0 & 1 \end{array} \right] \frac{\begin{bmatrix} i_{\text{int},j} \\ i_{\text{int},k} \\ v_{\text{int},j} \\ v_{\text{int},k} \end{bmatrix}}{=} 0$$

Driving points



Pair of terminals with *external* through current and across voltage

Algebraic equations

$$\left[M_1 \quad M_2 \mid M_3 \quad M_4 \right] \begin{bmatrix} i_{\text{int}} \\ v_{\text{int}} \\ i_{\text{ext}} \\ v_{\text{ext}} \end{bmatrix} = 0$$

Circuit behavior

Element laws, driving points, conservation laws:

$$\left[\begin{array}{cc|cc} M_1 & M_2 & M_3 & M_4 \\ \hline \text{diag } P_k \left(\frac{d}{dt} \right) & \text{diag } Q_k \left(\frac{d}{dt} \right) & 0 & 0 \end{array} \right] \begin{bmatrix} i_{\text{int}} \\ v_{\text{int}} \\ i_{\text{ext}} \\ v_{\text{ext}} \end{bmatrix} = 0 \quad (1)$$

Circuit behavior

Element laws, driving points, conservation laws:

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Full behavior

$$\mathcal{B} = \{(i, v) : (i, v) \in \mathcal{L}_{\text{loc}} \times \mathcal{L}_{\text{loc}} \text{ satisfy (1)}\}$$

Circuit behavior

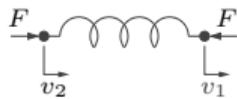
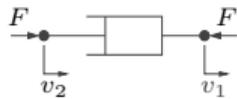
Element laws, driving points, conservation laws:

$$\left[\begin{array}{cc|cc} M_1 & M_2 & M_3 & M_4 \\ \hline \text{diag } P_k \left(\frac{d}{dt} \right) & \text{diag } Q_k \left(\frac{d}{dt} \right) & 0 & 0 \end{array} \right] \begin{bmatrix} i_{\text{int}} \\ v_{\text{int}} \\ i_{\text{ext}} \\ v_{\text{ext}} \end{bmatrix} = 0 \quad (1)$$

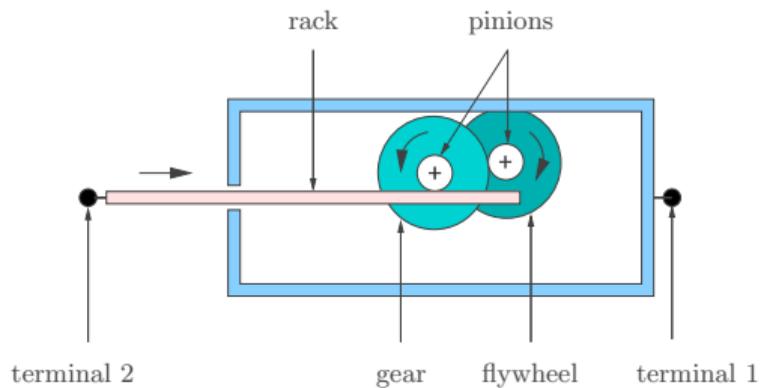
External behavior

$$\mathcal{B} = \left\{ (i_e, v_e) : \left(\begin{bmatrix} i_i \\ i_e \end{bmatrix}, \begin{bmatrix} v_i \\ v_e \end{bmatrix} \right) \in \mathcal{L}_{\text{loc}} \times \mathcal{L}_{\text{loc}} \text{ satisfy (1)} \right\}$$

Analogues

Mechanical	Electrical		
 <p>A coiled spring is shown between two terminals. On the left terminal, a force F is applied to the right, and the velocity v_2 is directed downwards. On the right terminal, a force F is applied to the left, and the velocity v_1 is directed downwards.</p>	 <p>An inductor is represented by a coiled wire between two terminals. On the left terminal, current i flows to the right, and the voltage v_2 is directed downwards. On the right terminal, current i flows to the left, and the voltage v_1 is directed downwards.</p>	spring	inductor
 <p>A rectangular mass is shown between two terminals. On the left terminal, a force F is applied to the right, and the velocity v_2 is directed downwards. On the right terminal, the velocity v_1 is indicated as $v_1 = 0$, representing a fixed wall.</p>	 <p>A capacitor is represented by two parallel vertical plates between two terminals. On the left terminal, current i flows to the right, and the voltage v_2 is directed downwards. On the right terminal, current i flows to the left, and the voltage v_1 is directed downwards.</p>	mass	capacitor
 <p>A damper is represented by a rectangular block between two terminals. On the left terminal, a force F is applied to the right, and the velocity v_2 is directed downwards. On the right terminal, a force F is applied to the left, and the velocity v_1 is directed downwards.</p>	 <p>A resistor is represented by a rectangular block between two terminals. On the left terminal, current i flows to the right, and the voltage v_2 is directed downwards. On the right terminal, current i flows to the left, and the voltage v_1 is directed downwards.</p>	damper	resistor

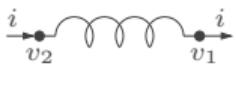
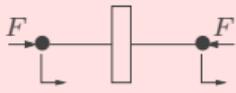
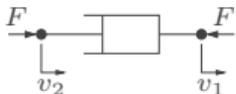
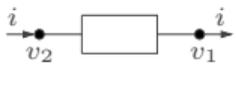
Analogues



Suppose the flywheel of mass m rotates by α radians per meter of relative displacement between the terminals. Then:

$$\mathbf{F} = (m\alpha^2) (\dot{\mathbf{v}}_2 - \dot{\mathbf{v}}_1)$$

Analogues

Mechanical	Electrical
 $Y(s) = \frac{k}{s}$ $\frac{dF}{dt} = k(v_2 - v_1)$ spring	 $Y(s) = \frac{1}{Ls}$ $\frac{di}{dt} = \frac{1}{L}(v_2 - v_1)$ inductor
 $Y(s) = bs$ $F = b \frac{d(v_2 - v_1)}{dt}$ inerter	 $Y(s) = Cs$ $i = C \frac{d(v_2 - v_1)}{dt}$ capacitor
 $Y(s) = c$ $F = c(v_2 - v_1)$ damper	 $Y(s) = \frac{1}{R}$ $i = \frac{1}{R}(v_2 - v_1)$ resistor

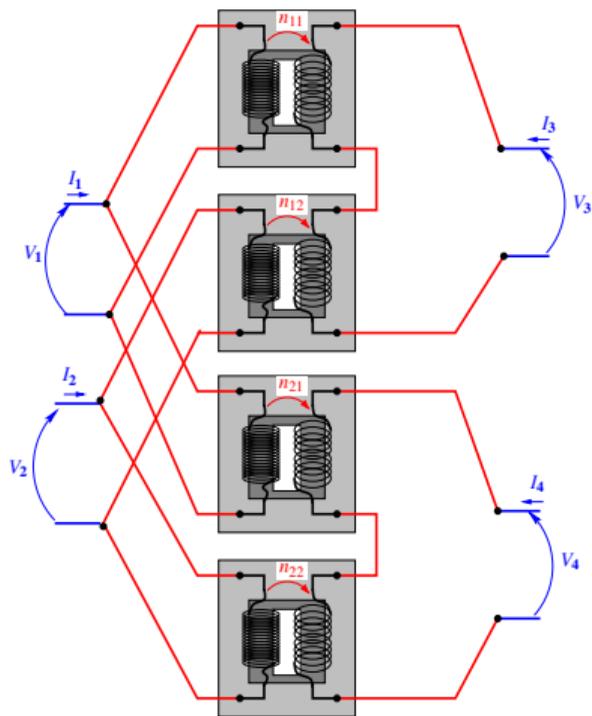
Network synthesis and state-space models

$$\left[\begin{array}{cc|cc} M_1 & M_2 & M_3 & M_4 \\ \hline \text{diag } P_i \left(\frac{d}{dt} \right) & \text{diag } Q_i \left(\frac{d}{dt} \right) & 0 & 0 \end{array} \right] \begin{bmatrix} i_{\text{int}} \\ v_{\text{int}} \\ i_{\text{ext}} \\ v_{\text{ext}} \end{bmatrix} = 0$$

Behavioral state-space model:

$$\mathcal{B}_s = \left\{ (x, u, y) : \frac{d}{dt}x = Ax + Bu, y = Cx + Du \right\}$$

Transformer Synthesis



$$\begin{bmatrix} V_1 \\ V_2 \\ I_3 \\ I_4 \end{bmatrix} = \begin{bmatrix} 0 & 0 & n_{11} & n_{12} \\ 0 & 0 & n_{21} & n_{22} \\ -n_{11} & -n_{21} & 0 & 0 \\ -n_{12} & -n_{22} & 0 & 0 \end{bmatrix} \begin{bmatrix} I_1 \\ I_2 \\ V_3 \\ V_4 \end{bmatrix}$$

Transformer Synthesis

$$\left[\begin{array}{cc|cc} 0 & 0 & N^T & -I \\ I & N & 0 & 0 \end{array} \right] \begin{bmatrix} i_a \\ i_b \\ v_a \\ v_b \end{bmatrix} = 0$$

Transformer and Gyrator Synthesis

Can synthesise

$$\left[R \mid -I \right] \begin{bmatrix} i \\ v \end{bmatrix} = 0$$

for any $R = -R^T$.

Transformer and Gyration Synthesis

Can synthesise

$$\left[R \mid -I \right] \begin{bmatrix} i \\ v \end{bmatrix} = 0$$

for any $R = -R^T$.

Factor:

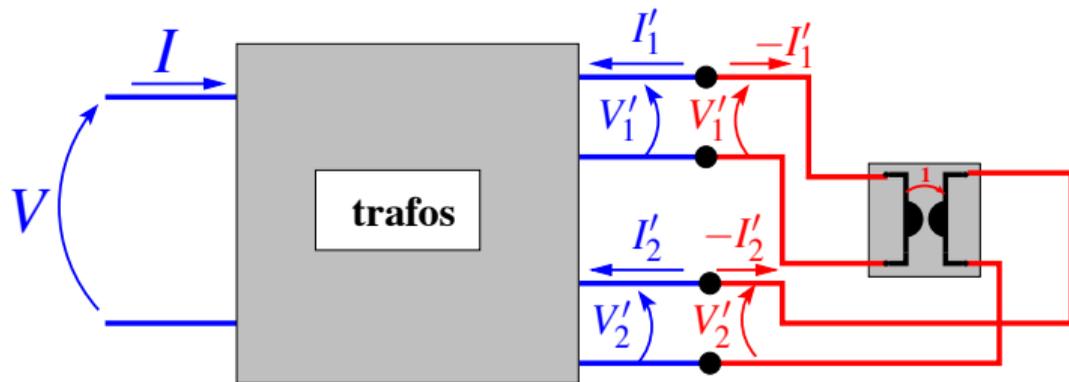
$$R = N^T \begin{bmatrix} 0 & I \\ -I & 0 \end{bmatrix} N$$

Transformer and Gyrator Synthesis

Synthesise with transformers:
$$\left[\begin{array}{cc|cc} 0 & 0 & N^T & -I \\ I & N & 0 & 0 \end{array} \right] \begin{bmatrix} i' \\ i \\ v' \\ v \end{bmatrix} = 0$$

Synthesise with gyrators:
$$\left[\begin{array}{cc|cc} 0 & -I & I & 0 \\ I & 0 & 0 & I \end{array} \right] \begin{bmatrix} i'' \\ v'' \end{bmatrix} = 0$$

Transformer and Gyrator Synthesis



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$$v = N^T v''$$

Transformer and Gyrator Synthesis

Synthesise with transformers:
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$$\left[\begin{array}{cc|cc} 0 & -I & I & 0 \\ I & 0 & 0 & I \end{array} \right] \begin{bmatrix} i'' \\ v'' \end{bmatrix} = 0$$

$$v = -N^T \begin{bmatrix} 0 & -I \\ I & 0 \end{bmatrix} i''$$

Transformer and Gyrator Synthesis

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$$v = N^T \begin{bmatrix} 0 & -I \\ I & 0 \end{bmatrix} i'$$

Transformer and Gyrator Synthesis

Synthesise with transformers:
$$\left[\begin{array}{cc|cc} 0 & 0 & N^T & -I \\ I & N & 0 & 0 \end{array} \right] \begin{bmatrix} i' \\ i \\ v' \\ v \end{bmatrix} = 0$$

Synthesise with gyrators:
$$\left[\begin{array}{cc|cc} 0 & -I & I & 0 \\ I & 0 & 0 & I \end{array} \right] \begin{bmatrix} i'' \\ v'' \end{bmatrix} = 0$$

$$v = -N^T \begin{bmatrix} 0 & -I \\ I & 0 \end{bmatrix} Ni = Ri$$

Resistor, Transformer, and Gyrator Synthesis...

Can synthesise

$$\left[R \mid -I \right] \begin{bmatrix} i \\ v \end{bmatrix} = 0$$

for any R such that $R + R^T \succeq 0$.

Resistor, Transformer, and Gyrator Synthesis...

Can synthesise

$$\left[R \mid -I \right] \begin{bmatrix} i \\ v \end{bmatrix} = 0$$

for any R such that $R + R^T \succeq 0$.

Factor:

$$\frac{1}{2} (R + R^T) = S^T S$$

Resistor, Transformer, and Gyrator Synthesis...

$$\text{Synth with trans and gyr: } \left[\begin{array}{cc|cc} 0 & S & I & 0 \\ -S^T & \frac{1}{2}(R^T - R) & 0 & I \end{array} \right] \begin{bmatrix} i' \\ i \\ v' \\ v \end{bmatrix} = 0$$

$$\text{Synthesise with resistors: } [-I \mid I] \begin{bmatrix} i'' \\ v'' \end{bmatrix} = 0$$

RLTG synthesis

Synthesise with RTG:

$$\left[\begin{array}{cc|cc} -A & -I & -B & 0 \\ C & 0 & D & -I \end{array} \right] \left[\begin{array}{c} i_{\text{int}} \\ v_{\text{int}} \\ i_{\text{ext}} \\ v_{\text{ext}} \end{array} \right] = 0$$

Synthesise with L

$$\left[I \frac{d}{dt} \mid -I \right] \left[\begin{array}{c} i' \\ v' \end{array} \right] = 0$$

RLTG synthesis

$$\left[\begin{array}{cc|cc} -A & -I & -B & 0 \\ C & 0 & D & -I \\ \hline I \frac{d}{dt} & I & 0 & 0 \end{array} \right] \begin{bmatrix} i_{\text{int}} \\ v_{\text{int}} \\ i_{\text{ext}} \\ v_{\text{ext}} \end{bmatrix} = 0$$

RLTG synthesis

$$\left[\begin{array}{cc|cc} I \frac{d}{dt} - A & 0 & -B & 0 \\ C & 0 & D & -I \\ \hline I \frac{d}{dt} & I & 0 & 0 \end{array} \right] \begin{bmatrix} i_{\text{int}} \\ v_{\text{int}} \\ i_{\text{ext}} \\ v_{\text{ext}} \end{bmatrix} = 0$$

RLTG synthesis

$$\left[\begin{array}{ccc|c} I \frac{d}{dt} - A & -B & 0 & 0 \\ C & D & -I & 0 \\ \hline I \frac{d}{dt} & 0 & 0 & I \end{array} \right] \begin{bmatrix} x \\ i_{\text{ext}} \\ v_{\text{ext}} \\ v_{\text{int}} \end{bmatrix} = 0$$

RLTG synthesis

Can synthesise any state-space model for which

$$\begin{bmatrix} -A & -B \\ C & D \end{bmatrix} + \begin{bmatrix} -A & -B \\ C & D \end{bmatrix}^T \preceq 0.$$

An equivalent characterisation

Every external RLCTG network behavior admits a state-space realisation with

$$\begin{bmatrix} -A & -B \\ C & D \end{bmatrix} + \begin{bmatrix} -A & -B \\ C & D \end{bmatrix}^T \preceq 0.$$

Special cases

Lossless Networks	T	N/A	$\left[\begin{array}{c cc} \vdots & & \\ \hline & 0 & D_{12} \\ \hline & -D_{12}^T & 0 \end{array} \right]$	-
	LT	$-I$	$\left[\begin{array}{c cc} 0 & B_1 & 0 \\ \hline B_1^T & 0 & D_{12} \\ \hline 0 & -D_{12}^T & 0 \end{array} \right]$	-
	CT	I	$\left[\begin{array}{c cc} 0 & 0 & B_2 \\ \hline 0 & 0 & D_{12} \\ \hline B_2^T & -D_{12}^T & 0 \end{array} \right]$	-
	LCT	$\begin{bmatrix} I & 0 \\ 0 & -I \end{bmatrix}$	$\left[\begin{array}{cc cc} 0 & A_{12} & 0 & B_{12} \\ -A_{12}^T & 0 & B_{21} & 0 \\ \hline 0 & B_{21}^T & 0 & D_{12} \\ B_{12}^T & 0 & -D_{12}^T & 0 \end{array} \right]$	-

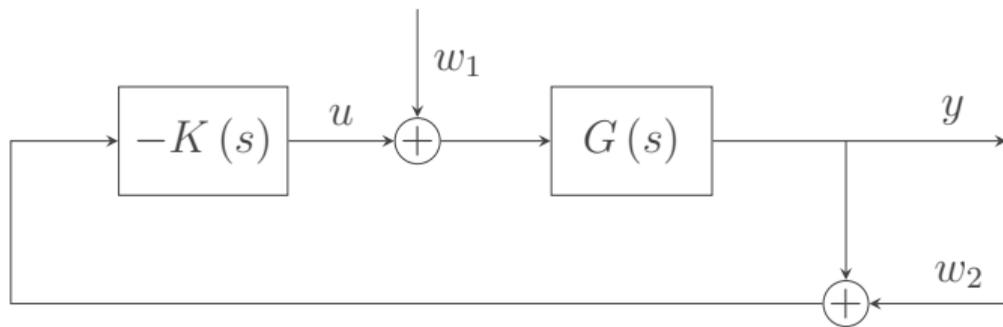
Special cases

Lossy Networks	RT	N/A	$\left[\begin{array}{c cc} & & \\ \hline & D_{11} & D_{12} \\ & -D_{12}^T & D_{22} \end{array} \right]$	$D_{11} \succeq 0, D_{22} \succeq 0$
	RLT	$-I$	$\left[\begin{array}{c cc} A & B_1 & B_2 \\ \hline B_1^T & D_{11} & D_{12} \\ -B_2^T & -D_{12}^T & D_{22} \end{array} \right]$	$\begin{bmatrix} -A & B_2 \\ B_2^T & D_{22} \end{bmatrix} \succeq 0, D_{11} \succeq 0$
	RCT	I	$\left[\begin{array}{c cc} A & B_1 & B_2 \\ \hline -B_1^T & D_{11} & D_{12} \\ B_2^T & -D_{12}^T & D_{22} \end{array} \right]$	$\begin{bmatrix} -A & B_1 \\ B_1^T & D_{11} \end{bmatrix} \succeq 0, D_{22} \succeq 0$

Open problems

- Transformerless synthesis
- Synthesise resistive '4 ports'
- ...

Exploit structure for Optimal Control

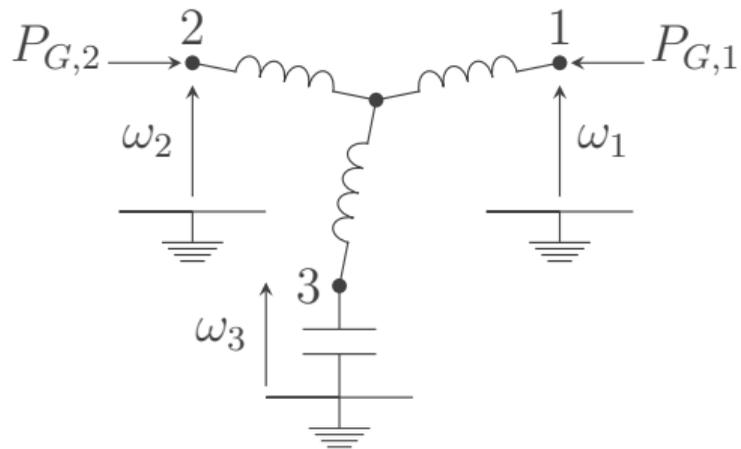


Exploit structure for Optimal Control

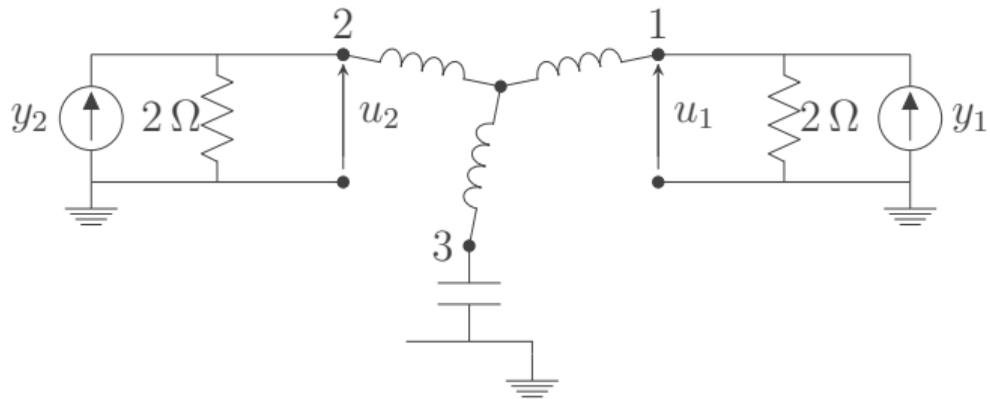
Optimal Controller:

$$\begin{bmatrix} A_c & B_c \\ \hline C_c & D_c \end{bmatrix} = \begin{cases} \begin{bmatrix} A - 2BB^T & B \\ \hline B^T & 0 \end{bmatrix} & \text{in the } H_2 \text{ case;} \\ \begin{bmatrix} \sqrt{2}I \end{bmatrix} & \text{in the } H_\infty \text{ case;} \end{cases}$$

Example I



Example I



Example I

Reasonable starting point for grid forming inverter design:

1. Easy to scale
2. Inherits network structure
3. Nothing to do with sparsity!

Example II

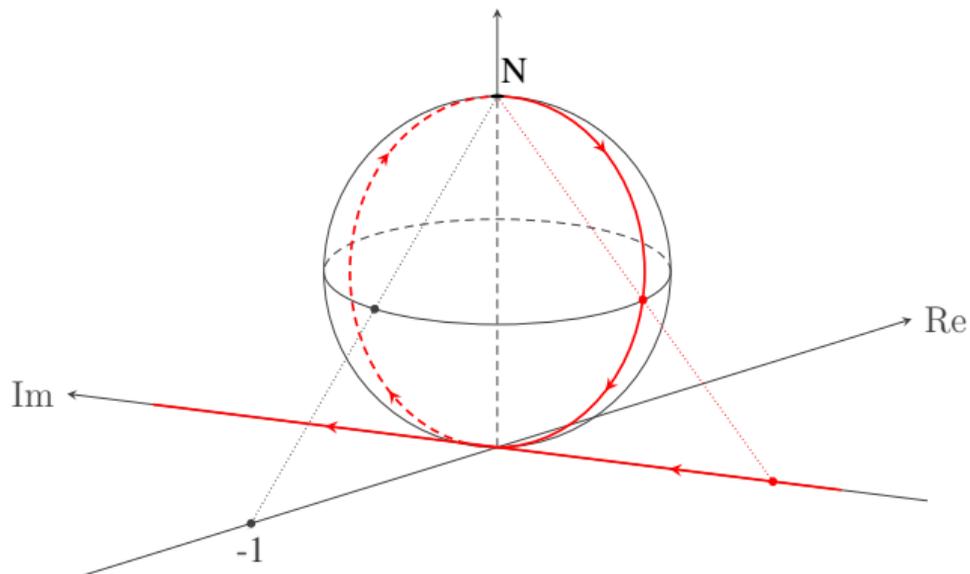


Figure 4: Illustration of the observation about the optimal control law from Remark 3. In the scalar case, the H_∞ norm of

$$\begin{bmatrix} G(s) \\ I \end{bmatrix} (I + K(s)G(s))^{-1} \begin{bmatrix} K(s) & I \end{bmatrix},$$

Example II

The objective of constrained least squares is to find an $\bar{x} \in \mathbb{R}^n$ that satisfies

$$\min_{\bar{x} \in \mathbb{R}^n} \|A\bar{x} - b\|_2, \text{ s.t. } C\bar{x} = d, \quad (33)$$

where $A \in \mathbb{R}^{m \times n}$, $C \in \mathbb{R}^{p \times n}$, $b \in \mathbb{R}^m$ and $d \in \mathbb{R}^p$ are the problem data. Constrained least squares encompasses a very broad class of problems including, for example, finite horizon LQR, and includes standard least squares and minimum norm solutions to a set of linear equations as special cases ($p = 0$, and $A = I$ and $b = 0$, respectively). The solution to eq. (33) can be obtained from the Karush-Kuhn-Tucker conditions

$$\begin{bmatrix} -A^T A & -C^T \\ C & 0 \end{bmatrix} \begin{bmatrix} \bar{x} \\ \bar{z} \end{bmatrix} = \begin{bmatrix} -A^T b \\ d \end{bmatrix}, \quad (34)$$

Example II

To this end, consider the system

$$\begin{aligned}\frac{d}{dt}x &= \begin{bmatrix} 0 & -C^T \\ C & 0 \end{bmatrix} x + \begin{bmatrix} A^T \\ 0 \end{bmatrix} (u + w_1 - r_1) + \begin{bmatrix} 0 \\ I \end{bmatrix} r_2, \\ y &= [A \ 0]x + w_2.\end{aligned}\tag{35}$$

Example II

eq. (35) that the closed loop system becomes

$$\begin{aligned}\frac{d}{dt}x &= \begin{bmatrix} -A^T A & -C^T \\ C & 0 \end{bmatrix} x + \begin{bmatrix} A^T \\ 0 \end{bmatrix} (w_1 - r_1) + \begin{bmatrix} 0 \\ I \end{bmatrix} r_2, \\ y &= \begin{bmatrix} A & 0 \end{bmatrix} x + w_2.\end{aligned}$$

Therefore by applying the step inputs $r_1 = bH(t)$ and $r_2 = dH(t)$, where $H(t)$ denotes the unit step, we see that

$$\lim_{t \rightarrow \infty} x(t) = \begin{bmatrix} \bar{x} \\ \bar{z} \end{bmatrix}.$$